IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: R. TSUCHIYA et al

Serial No. 10/372,329 Filed: February 25, 2003

Group Art Unit:

2814

Examiner:

N.V. Ngo

For:

MIS SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Mail Stop Patent Application P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, the Applicants inform the Examiner of the documents cited during prosecution of the parent application, U.S. Serial No. 10/372,329.

The Applicants request the Examiner to initial and return a copy of the attached PTO-1449 form as an indication that the references have been considered.

Respectfully submitted,

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Date: April 16, 2004

FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		NIT-368-02		SERIAL NO.			
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* EXAMINER INITIAL		DOCUMENT	DATE		NAME	CLASS	SUBCLASS	FILING (If Appr	DATE opriate)
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	AL	7-245391	09/19/95	Japan (w/ Engl		_			
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)									
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* EXAMINER next communical	R: Initia	al if reference considere applicant.	ed, whether or not citation	n is in conformance with MPEP	609; Draw line through citation if not in	conformance and	not considered. Includ	e copy of thi	s form with